

SNES S-PPU1 Pinout by [Jonathon W. Donaldson \(jwdonal\)](#) (special thanks to [Martin Korth \(nocash\)](#))

Designator	Name	Desc	QFP100P1870X2470-64AM	Type	Owner	Show	Number	Name
1	TST1	Unknown (GND)	1	Input	1	True	True	TST1
2	TST0	Unknown (GND)	2	Input	1	True	True	TST0
3	PVA R\D\	5A22 B-Bus Read Strobe (Active-Low)	3	Input	1	True	True	PVA R\D\
4	PVA W R\	5A22 B-Bus Write Strobe (Active-Low)	4	Input	1	True	True	PVA W R\
5	PA7	5A22 B-Bus Address	5	Input	1	True	True	PA7
6	PA6	5A22 B-Bus Address	6	Input	1	True	True	PA6
7	PA5	5A22 B-Bus Address	7	Input	1	True	True	PA5
8	PA4	5A22 B-Bus Address	8	Input	1	True	True	PA4
9	PA3	5A22 B-Bus Address	9	Input	1	True	True	PA3
10	PA2	5A22 B-Bus Address	10	Input	1	True	True	PA2
11	PA1	5A22 B-Bus Address	11	Input	1	True	True	PA1
12	PA0	5A22 B-Bus Address	12	Input	1	True	True	PA0
13	VCC	Supply	13	Power	1	True	True	VCC
14	D7	5A22 Data Bus	14	I/O	1	True	True	D7
15	D6	5A22 Data Bus	15	I/O	1	True	True	D6
16	D5	5A22 Data Bus	16	I/O	1	True	True	D5
17	D4	5A22 Data Bus	17	I/O	1	True	True	D4
18	D3	5A22 Data Bus	18	I/O	1	True	True	D3
19	D2	5A22 Data Bus	19	I/O	1	True	True	D2
20	D1	5A22 Data Bus	20	I/O	1	True	True	D1
21	D0	5A22 Data Bus	21	I/O	1	True	True	D0
22	GND	Supply	22	Power	1	True	True	GND
23	HVCMODE	Home Video Computer (a.k.a. Famicom) Mode (purpose unknown since SNES can play Famicom games already) (GND)	23	Input	1	True	True	HVCMODE
24	PALMODE	PAL Video Mode Enable (VCC=PAL, GND=NTSC)	24	Input	1	True	True	PALMODE
25	MA S T E R\	Unknown (GND)	25	Input	1	True	True	MA S T E R\
26	E X T S I Y N C\	Unknown (VCC)	26	Input	1	True	True	E X T S I Y N C\
27	NC	GND	27	Passive	1	True	True	NC
28	VDB0	VRAM Data Bus (High Byte)	28	I/O	1	True	True	VDB0
29	VDB1	VRAM Data Bus (High Byte)	29	I/O	1	True	True	VDB1
30	VDB2	VRAM Data Bus (High Byte)	30	I/O	1	True	True	VDB2
31	VDB3	VRAM Data Bus (High Byte)	31	I/O	1	True	True	VDB3
32	VDB4	VRAM Data Bus (High Byte)	32	I/O	1	True	True	VDB4
33	VDB5	VRAM Data Bus (High Byte)	33	I/O	1	True	True	VDB5
34	VDB6	VRAM Data Bus (High Byte)	34	I/O	1	True	True	VDB6
35	VDB7	VRAM Data Bus (High Byte)	35	I/O	1	True	True	VDB7
36	VCC	Supply	36	Power	1	True	True	VCC
37	VDA0	VRAM Data Bus (Low Byte)	37	I/O	1	True	True	VDA0

Designator	Name	Desc	QFP100P1870X2470-64AM	Type	Owner	Show	Number	Name
38	VDA1	VRAM Data Bus (Low Byte)	38	I/O	1	True	True	VDA1
39	VDA2	VRAM Data Bus (Low Byte)	39	I/O	1	True	True	VDA2
40	VDA3	VRAM Data Bus (Low Byte)	40	I/O	1	True	True	VDA3
41	VDA4	VRAM Data Bus (Low Byte)	41	I/O	1	True	True	VDA4
42	VDA5	VRAM Data Bus (Low Byte)	42	I/O	1	True	True	VDA5
43	VDA6	VRAM Data Bus (Low Byte)	43	I/O	1	True	True	VDA6
44	VDA7	VRAM Data Bus (Low Byte)	44	I/O	1	True	True	VDA7
45	GND	Supply	45	Power	1	True	True	GND
46	VA15	VRAM Address Bus (NC)	46	Output	1	True	True	VA15
47	VA14	VRAM Address Bus (controls selection of upper/lower byte)	47	Output	1	True	True	VA14
48	VAB13	VRAM Address Bus (High Byte)	48	Output	1	True	True	VAB13
49	VAB12	VRAM Address Bus (High Byte)	49	Output	1	True	True	VAB12
50	VAB11	VRAM Address Bus (High Byte)	50	Output	1	True	True	VAB11
51	VAB10	VRAM Address Bus (High Byte)	51	Output	1	True	True	VAB10
52	VAB9	VRAM Address Bus (High Byte)	52	Output	1	True	True	VAB9
53	VAB8	VRAM Address Bus (High Byte)	53	Output	1	True	True	VAB8
54	VAB7	VRAM Address Bus (High Byte)	54	Output	1	True	True	VAB7
55	VAB6	VRAM Address Bus (High Byte)	55	Output	1	True	True	VAB6
56	VAB5	VRAM Address Bus (High Byte)	56	Output	1	True	True	VAB5
57	VAB4	VRAM Address Bus (High Byte)	57	Output	1	True	True	VAB4
58	VAB3	VRAM Address Bus (High Byte)	58	Output	1	True	True	VAB3
59	VAB2	VRAM Address Bus (High Byte)	59	Output	1	True	True	VAB2
60	VAB1	VRAM Address Bus (High Byte)	60	Output	1	True	True	VAB1
61	VAB0	VRAM Address Bus (High Byte)	61	Output	1	True	True	VAB0
62	VCC	Supply	62	Power	1	True	True	VCC
63	VAA13	VRAM Address Bus (Low Byte)	63	Output	1	True	True	VAA13
64	VAA12	VRAM Address Bus (Low Byte)	64	Output	1	True	True	VAA12
65	VAA11	VRAM Address Bus (Low Byte)	65	Output	1	True	True	VAA11
66	VAA10	VRAM Address Bus (Low Byte)	66	Output	1	True	True	VAA10
67	VAA9	VRAM Address Bus (Low Byte)	67	Output	1	True	True	VAA9

Designator	Name	Desc	QFP100P1870X2470-64AM	Type	Owner	Show	Number	Name
68	VAA8	VRAM Address Bus (Low Byte)	68	Output	1	True	True	VAA8
69	VAA7	VRAM Address Bus (Low Byte)	69	Output	1	True	True	VAA7
70	VAA6	VRAM Address Bus (Low Byte)	70	Output	1	True	True	VAA6
71	VAA5	VRAM Address Bus (Low Byte)	71	Output	1	True	True	VAA5
72	VAA4	VRAM Address Bus (Low Byte)	72	Output	1	True	True	VAA4
73	VAA3	VRAM Address Bus (Low Byte)	73	Output	1	True	True	VAA3
74	VAA2	VRAM Address Bus (Low Byte)	74	Output	1	True	True	VAA2
75	VAA1	VRAM Address Bus (Low Byte)	75	Output	1	True	True	VAA1
76	VAA0	VRAM Address Bus (Low Byte)	76	Output	1	True	True	VAA0
77	GND	Supply	77	Power	1	True	True	GND
78	V\A\W\R\	VRAM Write Strobe (High Byte) (Active-Low)	78	Output	1	True	True	V\A\W\R\
79	V\B\W\R\	VRAM Write Strobe (Low Byte) (Active-Low)	79	Output	1	True	True	V\B\W\R\
80	V\R\D\	VRAM Read Strobe (16-bit) (Active-Low)	80	Output	1	True	True	V\R\D\
81	VCC	Supply	81	Power	1	True	True	VCC
82	CHR3	Unknown (connected to PPU2)	82	Passive	1	True	True	CHR3
83	CHR2	Unknown (connected to PPU2)	83	Passive	1	True	True	CHR2
84	CHR1	Unknown (connected to PPU2)	84	Passive	1	True	True	CHR1
85	CHR0	Unknown (connected to PPU2)	85	Passive	1	True	True	CHR0
86	PRI01	Unknown (connected to PPU2)	86	Passive	1	True	True	PRI01
87	PRI00	Unknown (connected to PPU2)	87	Passive	1	True	True	PRI00
88	COLOR2	Unknown (connected to PPU2)	88	Passive	1	True	True	COLOR2
89	COLOR1	Unknown (connected to PPU2)	89	Passive	1	True	True	COLOR1
90	COLOR0	Unknown (connected to PPU2)	90	Passive	1	True	True	COLOR0
91	V\C\L\D\	Unknown (connected to PPU2)	91	Passive	1	True	True	V\C\L\D\
92	H\C\L\D\	Unknown (connected to PPU2)	92	Passive	1	True	True	H\C\L\D\
93	5\M\O\U\T\	Dot Clock Out (5.37MHz) (connected to PPU2)	93	Output	1	True	True	5\M\O\U\T\
94	O\V\E\R\	Unknown (connected to PPU2)	94	Output	1	True	True	O\V\E\R\
95	FIELD	Unknown (connected to PPU2)	95	Passive	1	True	True	FIELD
96	GND	Supply	96	Power	1	True	True	GND
97	5\M\I\N\	Dot Clock In (5.37MHz) (driven by local /5MOUT)	97	Input	1	True	True	5\M\I\N\
98	R\E\S\E\T\	Reset (from PPU2)	98	Input	1	True	True	R\E\S\E\T\

Designator	Name	Desc	QFP100P1870X2470-64AM	Type	Owner	Show	Number	Name
99	TST2	Unknown (GND)	99	Input	1	True	True	TST2
100	XIN	Master Clock (21.47727MHz) (from Osc)	100	Input	1	True	True	XIN